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jc973 U.S. PTO  
12/27/01

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DATE OF DEPOSIT: DECEMBER 27, 2001

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*C. H. Phelps*

(SIGNATURE)

[illegible]

Inventor Tae-Sub CHANG and Dong-Ho LEE:  
For: HIGH FREQUENCY SEMICONDUCTOR CHIP PACKAGE AND  
SUBSTRATE

Enclosures:

- |                                     |   |  |
|-------------------------------------|---|--|
| <input checked="" type="checkbox"/> | Specification (pages 1-9); claims (pages 10-13); abstract (page 14) |  |
| <input checked="" type="checkbox"/> | FIVE sheet(s) of FORMAL drawings                                    |  |
| <input checked="" type="checkbox"/> | Declaration or Combined Declaration and Power of Attorney           |  |
| <input checked="" type="checkbox"/> | Newly executed (original or copy)                                   |  |
| <input checked="" type="checkbox"/> | Assignment with cover sheet   |  |
|                                     | Assignee Name and Address:  | Samsung Electronics Co., Ltd.<br>Suwon-city, Kyungki-do, Korea |
| <input checked="" type="checkbox"/> | Certified copy of priority document                                 |  |
| <input checked="" type="checkbox"/> | Return Postcard   |  |

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PTO Form 2038 authorizing credit card payment for the above-listed fees is enclosed.  
Any deficiency or overpayment should be charged or credited to deposit account  
number 13-1703. A duplicate copy of this sheet is enclosed.



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PATENT TRADEMARK OFFICE

Respectfully submitted,

MARGER JOHNSON & McCOLLOM, P.C.

Alan T. McCollom

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PATENT APPLICATION SERIAL NO. \_\_\_\_\_

U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICE  
FEE RECORD SHEET

01/14/2002 SDIRETA1 00000250 10040868

01 FC:101	740.00 OP
02 FC:103	126.00 OP

PTO-1556  
(5/87)

\*U.S. GPO: 2000-468-987/39595